NATIONWIDE ROADSHOW ON
DIGITAL INDIA RISC-V VEGA PROCESSORS

Unleashing the Power of VEGA: Igniting Innovation Across India

NOVEMBER 17 - 18, 2023

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

PSG COLLEGE OF TECHNOLOGY, COIMBATORE

Every participant of the two-day workshop during the roadshow will receive a certificate from IEEE India Council and C-DAC

@VEGAProcessors   @vegaprocessor   @VegaProcessor

NATIONWIDE ROADSHOW ON DIR-V VEGA PROCESSORS

ABOUT THE EVENT

The DIR-V VEGA nationwide roadshow, jointly organized by IEEE India Council and C-DAC, MeitY, will be held concurrently at various centers across India. This comprehensive training program, encompassing hands-on sessions, offers a deep dive into the VEGA series of processors and ecosystem. The training covers both theoretical foundations and practical applications, including insights into ARIES development boards, SDK and application development. Experts from C-DAC, integral members of the VEGA Processor development team, will deliver the training sessions. This workshop is designed for a diverse audience, including students, faculty members, research scholars, and industry professionals. Certificates will be awarded upon successful completion of the workshop.

ABOUT THE COLLEGE

PSG College of Technology, a premier institution for engineering, started in 1951, is well equipped with modern facilities of world class standards. The college is governmentaided, autonomous, NAAC accredited, ISO 9001 certified and affiliated to Anna University. The college offers a total of 56 full time and part time programmes in Engineering, Technology, Science and Management at UG,PG and Doctoral levels. The institution holds extremely proud alumni at renowned institutions. The college maintains close interaction with several R&D institutions in India and abroad. It also has close collaborative links with industries. Several advanced centres are set up with financial support from the Ministry of Human Resources Development, DST and other agencies.

ABOUT THE DEPARTMENT

The Department of Electronics and Communication Engineering started in the year 1968, has been splendid in providing dynamic and quality engineers to the society till date. The Department has Undergraduate programme in Electronics & Communication Engineering and Post Graduate Programmes in Communication Systems, VLSI Design, Wireless Communication and Nanoscience & Technology offering high class technical experience to the students. In the Department, BE ECE, ME Communication Systems and ME VLSI Design Programmes have been accredited for 6 years (in 2018), 6 years (in 2017) and 6 years (in 2016) respectively by National Board of Accreditation (NBA).

@VEGAProcessors   @vegaprocessor   @VegaProcessor

NATIONWIDE ROADSHOW ON DIR-V VEGA PROCESSORS

REGISTRATION FEE

IEEE Member : Rs. 500/- , Non - IEEE Member : Rs. 600/- (Inclusive of Service Tax)
Registration fee includes access to all technical sessions, tea/snacks and a registration kit. Payment procedure details will be provided after participant list is released. The number of seats are limited and are on a first come first basis.

For more details & registration kindly visit the roadshow website

https://vegaprocessors.in/dirv-roadshow2023/

IMPORTANT DATES

Last Date for the Participants to Register for the Workshop : 25.10.2023
Announcement of the First List of Participants : 27.10.2023
Last date for Paying Registration Fee : 30.10.2023
Announcement of the Second List of Participants :: 31.10.2023
Last date for Paying the Registration Fee : 03.11.2023
Nationwide VEGA Workshop at Regional Venues : 17.11.2023 - 18.11.2023
Certificate Distribution to Participants : 30.11.2023

P.S.G
COLLEGE OF TECHNOLOGY

Limited seats are available at regional venues for the workshop, so register quickly to secure your spot. Shortlisted candidates will be informed by the regional venue coordinators through email for the registration fee payment and seat confirmation. A nominal registration fee may be applicable. Registration fee discount is available for IEEE members.

@VEGAProcessors   @vegaprocessor   @VegaProcessor

CENTRE FOR DEVELOPMENT OF ADVANCED COMPUTING

Digital India RISC-V Program
India Semiconductor Mission

इलेक्ट्रॉनिकी एवं
सूचना प्रौद्योगिकी मंत्रालय
MINISTRY OF
ELECTRONICS AND
INFORMATION TECHNOLOGY
सत्यमेव जयते

VEGA PROCESSOR

IEEE INDIA COUNCIL

NATIONWIDE ROADSHOW ON DIR-V VEGA PROCESSORS

ORGANIZING COMMITTEE

CHIEF PATRON

Shri. L. Gopalakrishnan
Managing Trustee

PATRON

Dr. K. Prakasan
Principal

CONVENOR

Dr. V. Krishnaveni
Professor & Head, ECE

COORDINATORS

Dr. P. Saravanan
Associate Professor, ECE

Mr. K. R. Radhakrishnan
Assistant Professor (Sr.Gr), ECE

Dr. D. Sivaraj
Assistant Professor (Sl.Gr), ECE

Dr. M. Alagappan
Assistant Professor (Sl.Gr), ECE

Mr. N. Saritakumar
Assistant Professor (Sl.Gr), ECE

ADDRESS FOR COMMUNIATION

Dr. P. Saravanan
Associate Professor, Department of ECE
PSG College of Technology, Peelamedu, Coimbatore - 641 004
Mobile : +91 98944 12300, 890 360 8680
Email : dps.ece@psgtech.ac.in, dpsaravanan@gmail.com

@VEGAProcessors   @vegaprocessor   @VegaProcessor

CDAC — CENTRE FOR DEVELOPMENT OF ADVANCED COMPUTING

Digital India RISC-V Program — India Semiconductor Mission

इलेक्ट्रॉनिकी एवं सूचना प्रौद्योगिकी मंत्रालय
MINISTRY OF ELECTRONICS AND INFORMATION TECHNOLOGY
सत्यमेव जयते

VEGA PROCESSOR

IEEE INDIA COUNCIL